



**IN THE SPECIFICATION**

Kindly amend the first paragraph on page 14 with the following:

Q' high, and conversely, the computation load is set to a relatively low level if the performance of the processor is gauged to be relatively low. That is, if the computation load estimated in step 180 based on the accumulated weight factors corresponds to 120 mega cycles per second and the available processing capability of the decoder 10 is limited to 100 mega cycles per second, the ratio ~~ratio~~ between these two values (i.e.,  $100/120=80\%$ ) is used as the scale factor for the IDCT 16 computation and/or the MC 24 computation. Thus, for example, only 80% of the CPU load of IDCT 16 can be dedicated to process the incoming data of the decoder 10. In this way, both the IDCT 16 and/or the MC 24 can be selectively adjusted to scale down the computation load to avoid frame drop or unexpected results associated with exceeding the maximum CPU load of the decoder 10. It is noted that the amount of scaling the CPU loads of the components of the decoder 10 can be varied according to the predetermined criteria (or weight factor) set by an operator and the available process capabilities of the decoder.